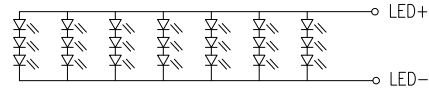
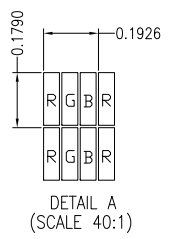


TFT PIN DESCRIPTION			
PIN	SYMBOL	PIN	SYMBOL
1	VLED+	26	G1
2	VLED+	27	G0
3	VLED-	28	R7
4	VLED-	29	R6
5	GND	30	R5
6	VCC	31	R4
7	VCC	32	R3
8	MODE	33	R2
9	DE	34	R1
10	VSYSN	35	R0
11	HSYN	36	GND
12	B7	37	DCLK
13	B6	38	GND
14	B5	39	LR
15	B4	40	UD
16	B3	41	VCC
17	B2	42	VCC
18	B1	43	NC
19	B0	44	RESET
20	G7	45	NC
21	G6	46	NC
22	G5	47	DITHB
23	G4	48	GND
24	G3	49	NC
25	G2	50	NC

RTP PIN DESCRIPTION	
PIN	SYMBOL
1	YU
2	XL
3	YD
4	XR



- NOTE:
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
 2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
 3. VIEWING DIRECTION: 12 O'CLOCK
 4. DRIVER IC: HX8264-E00+HX8664-B00 OR EK9713CA+EK73002ACBG
 5. OPERATING TEMP.: -20°C ~ 70°C
 6. STORAGE TEMP.: -30°C ~ 80°C
 7. LED BACKLIGHT: 21-CHIP WHITE LED, If=140mA, Vf=9.6V±1.2V
 8. SURFACE LUMINANCE: 380CD/M² (TYP.) WITH RTP
 9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
 10. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
 11. GENERAL TOLERANCE: ±0.3MM
 12. ROHS COMPLIANT

MIT MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	CAROL	2023.12.08		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0700S6T-13ARP
APPROVED BY:			UNIT: mm	DESCRIPTION:
VER.	01	FIRST ISSUE	SHEET NO.: 1/1	TFT + RTP MODULE

01	FIRST ISSUE	CAROL	2023.12.08
VER.	REVISED DESCRIPTION	REVISER	DATE